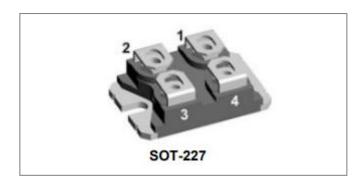






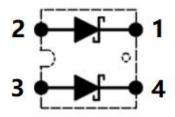
S4D80120S2 1200V SIC POWER SCHOTTKY RECTIFIER



Description

S4D80120S2 is a SiC Schottky rectifier packaged in SOT-277 case. The device is high voltage Schottky rectifier that has very low total conduction losses and very stable switching characteristics over temperature extremes. The S4D80120S2 is ideal for energy sensitive, high frequency applications in challenging environments.

Circuit Diagram



Applications

- Alternative energy inverters
- Power Factor Correction (PFC)
- Free-Wheeling diodes
- · Switching supply output rectification
- · Reverse polarity protection

Features

- 175°C T_J operation
- Ultra-low switching loss
- Switching speeds independent of operating temperature
- Low total conduction losses
- High forward surge current capability
- High package isolation voltage
- Terminals finish: 100% Pure Tin
- Pb Free Device
- All SMC parts are traceable to the wafer lot
- Additional electrical and life testing can be performed upon request

Maximum Ratings

Characteristics	Symbol	Condition	Max.	Units
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage	V_{RRM} V_{RWM}	-	1200	V
DC Blocking Voltage(per leg) Average Rectified Forward Current(per	V _R I _{F (AV)1}	T _C =25°C	128	Α
leg)	I _{F (AV)2}	T _C =155°C	41	А
Peak One Cycle Non-Repetitive Surge	I _{FSM1}	10ms, Half Sine pulse, T _C =25°C	340	Α
Current(per leg)	I _{FSM2}	10ms, Half Sine pulse, T _C =110°C	245	Α
	I _{FRM1}	10ms, Half Sine pulse , T _C =25°C	161	Α
Repetitive Peak Forward Surge Current(per leg)	I _{FRM2}	10ms, Half Sine pulse , T _C =110°C	91	Α
	P _{tot1}	T _C =25°C	667	W
Power Dissipation(per leg)	P _{tot2}	T _C =110°C	289	W
12t \/alua/nar lag\	∫i²t1	10ms, Tc=25℃	305	A ² s
l²t Value(per leg)	∫i²t2	10ms, Tc=25℃	300	A ² s

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Electrical Characteristics:

Characteristics	Symbol	Condition	Тур.	Max.	Units
Forward Voltage Drop*(per leg)	V _{F1}	@ 40A, Pulse, T _J = 25 °C	1.5	1.8	V
	V _{F2}	@ 40A, Pulse, T _J = 175 °C	2.2	3.0	V
Reverse Current*(per leg)	I_{R1} @ V_R = rated $V_{R_i}T_J$ = 25 °C		2	100	uA
	I _{R2}	@V _R = rated V _R , T _J = 175 °C	10	300	uA
Junction Capacitance(per leg)	Ст	VR=0V, f=1MHz, Tj=25℃,	2530	-	pF
Reverse Recovery Charge(per leg)	Qc	VR = 800 V, T _J =25°C	195	-	nC
Capacitance Stored Energy(per leg)	Ec	V _R = 800 V, T _J =25°C	100	-	μЈ

^{*} Pulse width < 300 µs, duty cycle < 2%

Thermal-Mechanical Specifications:

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	Τ _J	-	-55 to +175	°C
Storage Temperature	T_{stg}	-	-55 to +175	°C
Typical Thermal Resistance Junction to Case(per leg)	Rejc	DC operation, Tj=25°C	0.66	°C/W

Ordering Information

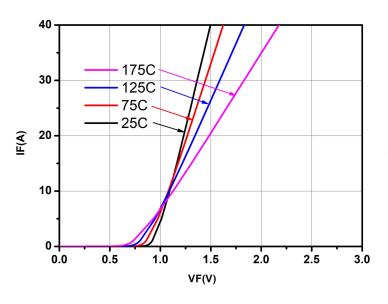
Device	Package	Shipping
S4D80120S2	SOT-277	12pcs / TUBE







Ratings and Characteristics Curves(per leg)



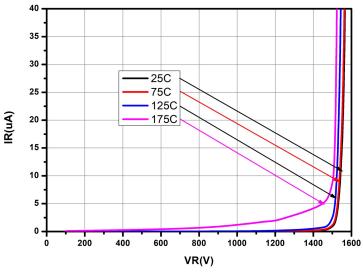
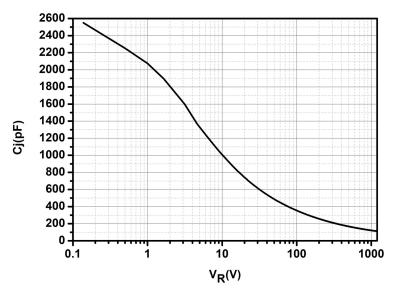


Fig.1-Typical Forward Voltage Characteristics

Fig.2-Typical Reverse Characteristics





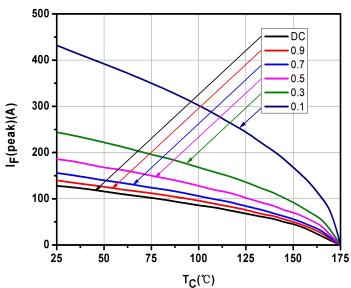
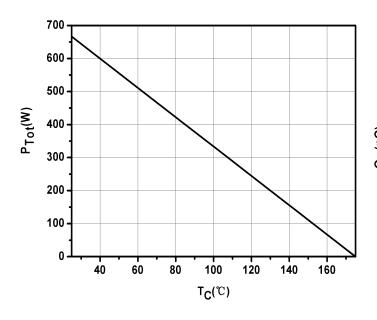


Fig.4-Current Derating









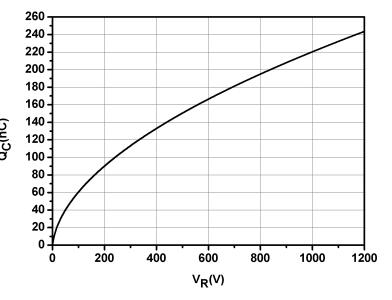


Fig.5-Power Derating

Fig.6-Total Capacitance Charge vs. Reverse Voltage

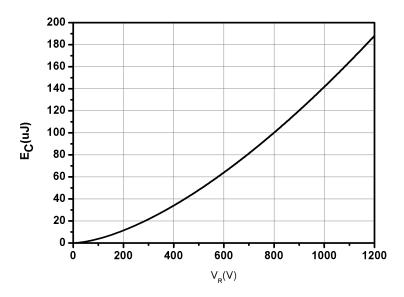


Fig.7-Capacitance Stored Energy

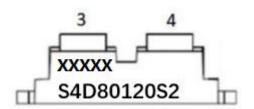
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Marking Diagram



Where XXXXX is YYWWL

 S4D
 = Device Type

 S2
 = Package type

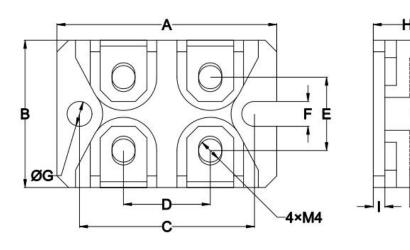
 80
 = Forward Current (80A)

 120
 = Reverse Voltage (1200V)

 SSG
 = SSG

SSG = SSG YY = Year WW = Week L = Lot Number

Mechanical Dimensions SOT-277



SYMBOL	Dimensions in millimeters		
	Min.	Max.	
А	37.8	38.2	
В	24.8	25.2	
С	29.9	30.5	
D	14.5	15.5	
E	12.2	13.2	
F	4.1	4.31	
G	φ4.1	φ4.31	
Н	11	12.5	
I	1.9	2.1	
K	4.3	6.5	

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